

First Named

Inventor : Bane Vasic et al.

Filed

: Herewith

For

: METHOD AND APPARATUS FOR

SUPPRESSING LOW

FREQUENCY CONTENT IN

DIGITAL DATA

Docket No.: S01.12-0864/STL 10205

10/084040 02/27/02

INFORMATION DISCLOSURE STATEMENT

Date of Deposit: February 27, 2002 Express Mail No. EL 844352135 US

Group Art Unit:

Examiner:

Commissioner for Patents Washington, D.C. 20231

Sir:

The patents or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 C.F.R. § 1.97. Copies of the patents or publications cited are enclosed.

TIME OF FILING

The information disclosure statement is being filed:

1. X with the application or within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever event occurs last. In accordance with 37 C.F.R. § 1.97(b), no statement or fee is required.

METHOD OF PAYMENT

_X	_No fee is	s re	quired.					
	Attached	is	a check	in	the	amount	of	\$ _•

The Director is authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 23-1123. A duplicate copy of this communication is enclosed.

> Respectfully submitted, WESTMAN, CHAMPLIN & KELLY, P.A.

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JVK/jme

FORM PTO-1449	Atty. Docket No.: S01.12-0864/STL 10205	Appl. No.:
	First Named Inventor:	E a C
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION	Bane Vasic et al.	8. 1940
DISCLOSURE STATEMENT	Filing Date	Group 🗚 😤
	Herewith	

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
AA	4,309,694	1/5/82	Henry	340	347	
AB	4,486,739	12/4/84	Franaszek et al.	340	347	
AC	5,073,776	12/17/91	Shigemori	341	106	
AD	5,497,384	3/5/96	Fredrickson et al.	371	43	
AE	5,537,424	7/16/96	Karabed et al.	371	43	
AF	5,608,397	3/4/97	Soljanin	341	58	
AG	5,731,768	3/24/98	Tsang	341	59	
АН	5,790,571	8/4/98	Varanasi	371	40.12	·
AI	5,801,649	9/1/98	Fredrickson	341	58	
AJ	6,052,072	4/18/00	Tsang et al.	341	59	
AK	6,111,834	8/29/00	Rub et al.	369	59	
AL	6,233,289	5/15/01	Fredrickson	375	341	
AM	6,198,582	3/6/01	Tsang et al.	360	40	·
AN	6,288,655	9/11/01	Tsang et al.	341	59	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	D.E. Knuth, "Efficient Balanced Codes," IEEE Transactions on Information Theory, Vol.IT-32, No. 1, pp.51-53, January 1986.				
AP	1 2 1 11	th, and A.M. Odlyzko, "Balancing Sets of mation Theory, Vol. 34, No. 1, pp. 128-130,			
AQ	New Line Coding Technique for High B	, and R.I. MacDonald, "Guided Scrambling: A it Rate Fiber Optic Transmission Systems," Vol. 39, No. 2, pp. 289-297, February 1991.			
XAMINER:		DATE CONSIDERED:			

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1	1449		Atty. Docket No.: S01.12-0864/STL 10205	Appl. No.:		
			First Named Inventor:			
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	DISCLOSURE STATEMENT		Filing Date	Group Art:		
			Herewith			
	OTHER ART (Including Author, Title,	, Date, I	Pertinent Pages, Etc.)			
AA	K.A.S Immink and L. Patrovics, "Perf Codes," IEEE Transactions on Communi 1997.					
AB	W. G. Bliss, "Circuitry For Performing Error Correction Calculations on Baseband Encoded Data to Eliminate Error Propagation," IBM Technical Disclosure Bulletin, Vol. 23, No. 10, pp. 4633-4634, March 1981.					
AC	J.L. Fan and A.R. Calderbank, "A Modified Concatenated Coding Scheme, with Applications to Magnetic Data Storage," IEEE Transactions on Information Theory, Vol. 44, No. 4, pp. 1565-1574, July 1998.					
AD	A.J. van Wijingaarden and K.A.S. Immink, "Constructions of Constrained Codes Employing Sequence Replacement Techniques," IEEE International Symposium on Information Theory, June 29 - July 4, 1997.					
AE	G.L. Pierobon, "Codes for Zero Spectral Density at Zero Frequency," IEEE Transactions on Information Theory, Vol. IT-30, No. 2, pp. 435-439, March 1984.					
AF	E. Elefheriou and R.D. Cideciyan, "On Codes Satisfying Mth-Order Running Digital Sum Constraints," IEEE Transactions on Information Theory, Vol. 37, No. 5, pp. 1294-1313, September 1991.					
AG	K.A.S. Immink, "Construction of Binary DC-Constrained Codes," Philips Journal of Research, Vol. 40, No. 1, pp. 22-39, 1985.					
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AI	K.A.S. Immink, "Spectral Null Codes," IEEE Transactions on Magnetics, Vol. 26, No. 2, pp. 1130-1135, March 1990.					
AJ	J V. Braun and A.J.E.M. Janssen, "On the Low-Frequency Suppression Performance of DC-Free Runlength-Limited Modulation Codes," IEEE Transactions on Consumer Electronics, Vol. 42, No. 4, pp. 939-945, November 1996.					
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